

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/239,659

Filed: January 29, 1999

Inventor(s):

Thomas A. Dye, Manuel J.
Alvarez II, and Peter GeigerTitle: Bandwidth Reducing
Memory Controller
Including Scalable
Embedded Parallel Data
Compression and
Decompression Engines§ Examiner: Kim, Hong Chong
§ Group/Art Unit: 2187
§ Atty. Dkt. No: 5143-01700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Jeffrey C. Hood

Signature

1/17/2003

Date

SUBMISSION OF FORMAL DRAWINGS**RECEIVED**

JAN 28 2003

Technology Center 2100

Official Draftsman
Commissioner for Patents.
Washington, D.C. 20231

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings (Figures 1-36b on 34 sheets) be accepted for filing.

Respectfully submitted,

Jeffrey C. Hood
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ATTORNEY FOR APPLICANTS

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